

January 1994

DESCRIPTION

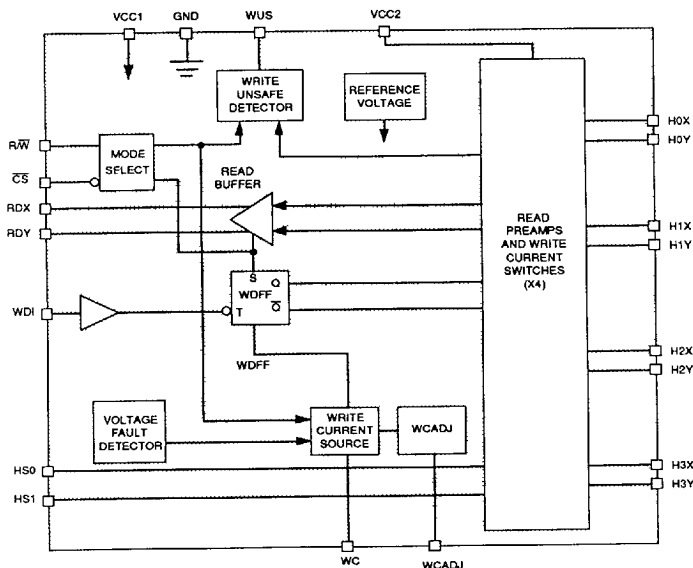
The SSI 32R2030A/2031A are bipolar monolithic integrated circuits designed for use with two-terminal thin-film recording heads. They provide a low noise read amplifier, write current control, and data protection circuitry for up to four channels. The SSI 32R2030AR/2031AR option provides internal 700Ω damping resistors. Power supply fault protection is provided by disabling the write current generator during power sequencing. System write to read recovery time is significantly improved by controlling the read channel common mode output voltage shift in the write mode. The 32R2031A option provides for an additional feature providing the user with a controllable write current adjustment feature.

The SSI 32R2030A/2031A require only +5V power supplies and are available in a variety of packages.

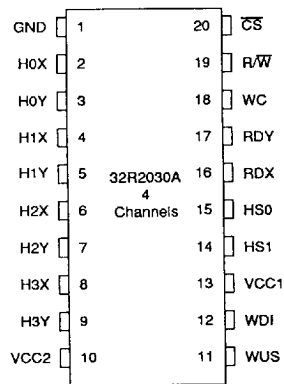
FEATURES

- **5V ±10%**
- **Low power**
 - PD = 175 mW read mode (Nom)
- **High Performance:**
 - Read mode gain = 250 V/V
 - Input noise = 0.85 nV/√Hz max
 - Input capacitance = 35 pF max
 - Write current range = 10-35 mA
- **Designed for two-terminal thin-film heads or MIG heads up to 5 μH**
- **Programmable write current source**
- **Write unsafe detection**
- **Enhanced system write to read recovery time**
- **Power supply fault protection**
- **Head short to ground protection**

BLOCK DIAGRAM



PIN DIAGRAM



20-PIN SOL

CAUTION: Use handling procedures necessary for a static sensitive component.

0194 - rev.

WCADJ available on the 32R2031A 24-pin option only

■ 8253965 0009467 284 ■ SIL

SSI 32R2030A/2031A

5V, 2, 4-Channel Thin-Film

Read/Write Device

CIRCUIT OPERATION

The SSI 32R2030A/2031A has the ability to address up to 4 two-terminal thin-film heads and provide write drive or read amplification. Head selection and mode control are described in Tables 2 and 3. The TTL inputs R/W and CS have internal pull-up resistors to prevent an accidental write condition. HS0, and HS1 have internal pulldowns. Internal clamp circuitry will protect the IC from a head short to ground condition in any mode.

TABLE 1: Mode Select

\overline{CS}	R/W	Mode
0	0	Write
0	1	Read
1	0	Idle
1	1	Idle

TABLE 2: Head Select

HS1	HS0	Head
0	0	0
0	1	1
1	0	2
1	1	3

PIN DESCRIPTION

NAME	TYPE	DESCRIPTION
HS0, HS1	I	Head Select: selects one of four heads
\overline{CS}	I	Chip Select: a high inhibits the chip
R/W †	I	Read/Write : a high selects Read mode
WUS †	O	Write Unsafe: a high indicates an unsafe writing condition
WDI †	I	Write Data In: changes the direction of the current in the recording head
H0X - H7X; H0Y - H7Y	I/O	X, Y Head Connections
RDX, RDY †	O	X, Y Read Data: differential read data output
WC †		Write Current: used to set the magnitude of the write current
WCADJ* †		Write Current Adjust: Used to decrease the write current by a finite amount
VCC1	I	+5V Supply
VCC2	I	+5V Supply for Write current drivers
GND	I	Ground
*Available on 32R2031A 24-pin option only		
† These signals can be wire OR'ed		

SSI 32R2030A/2031A

5V, 2, 4-Channel Thin-Film Read/Write Device

WRITE MODE

Taking both \overline{CS} and R/\overline{W} low selects write mode which configures the SSI 32R2030A/2031A as a current switch and activates the Write Unsafe (WUS) detector circuitry. Head current is toggled between the X and Y side of the selected head on each high to low transition of the Write Data Input (WDI). The WDI input pulse width requirement is amplitude dependent and pull ups are recommended at higher data rates, please refer to the WDI pulse width specifications. Note that a preceding read or idle mode select initializes the Write Data Flip-Flop to pass write current through the "X" side of the head. The magnitude of the write current (0-pk) is given by:

$$I_w = \frac{K \cdot V_{WC}}{R_{WC}}$$

R_{WC} is connected from pin WC to GND. Note the actual head current I_x, y is given by:

$$I_x, y = \frac{I_w}{1 + R_h/R_d}$$

Where:

R_h = Head resistance plus external wire
resistance

R_d = Damping resistance

Any of the following conditions will be indicated as a high level on the Write Unsafe, WUS, open collector output.

- WDI frequency too low
- Device in Read mode
- Chip disabled
- No write current

After fault condition is removed, one negative transition on WDI is required to clear WUS.

The 32R2031A adds a feature which allows the user to adjust the I_w current by a finite amount. The WCADJ pin is used to adjust write current for write operations on different zones of the disk. It is used by switching a separate write current adjust resistor in and out on the WCADJ pin or by connecting a DAC to that pin to sink a controllable amount of current. The WCADJ pin is nominally biased to $V_{CC}/2$. Sinking current from this

pin to ground will divert a proportional amount of current from the actual head current while maintaining a constant current through the WC resistor and VCC. Allowing WCADJ to float or pulling it high will cut off the circuit and it will have no effect. For example, if the nominal head current is set to 30 mA through WC with WCADJ open, then for a 7.25 mA head current decrease, a 10 k Ω resistor would be connected from the WCADJ pin to ground. A TTL gate could be used as a switch with a small degradation in accuracy. To perform the same function, a DAC could be used, by programming it to sink 0.25 mA from the WCADJ pin.

$$I_w \text{ head (Decrease)} = (29 \cdot V_{WCADJ} / R_{WCADJ})$$

Where:

V_{WCADJ} = Voltage on WCADJ pin = $V_{CC}/2$

R_{WCADJ} = Write current adjust setting resistor

VOLTAGE FAULT

A voltage Fault detection circuit improves data security by disabling the write current generator during a voltage fault or power startup regardless of mode.

READ MODE

The Read mode configures the SSI 32R2030A/2031A as a low noise differential amplifier and deactivates the write current generator. The RDX and RDY output are driven by emitter followers. They should be AC coupled to the load. The (X,Y) inputs are non-inverting to the (X,Y) outputs.

Note that in Idle or Write mode, the read amplifier is deactivated and RDX, RDY outputs become high impedance. This facilitates multiple R/W applications (wired-OR RDX, RDY) and minimizes voltage drifts when switching from Write to Read mode. Note also that the write current source is deactivated for both the Read and Idle mode.

IDLE MODE

Taking \overline{CS} high selects the idle mode which switches the RDX and RDY outputs into a high impedance state and deactivates the device. Power consumption in this mode is held to a minimum.

SSI 32R2030A/2031A

5V, 2, 4-Channel Thin-Film Read/Write Device

ELECTRICAL SPECIFICATIONS

ABSOLUTE MAXIMUM RATINGS

Operation above maximum ratings may permanently damage the device.

PARAMETER		RATING
DC Supply Voltage	VCC1	-0.3 to +7 VDC
	VCC2	-0.3 to +7 VDC
Write Current	I _W	80 mA
Digital Input Voltage	V _{in}	-0.3 to VCC1 +0.3 VDC
Head Port Voltage	V _H	-0.3 to VCC2 +0.3 VDC
Output Current: RDX, RDY	I _O	-10 mA
	WUS	+12 mA
Storage Temperature	T _{stg}	-65 to +150°C

RECOMMENDED OPERATING CONDITIONS

PARAMETER		RATING
DC Supply Voltage	VCC1	5 ±10% VDC
	VCC2	5 ±10% VDC
Operating Junction Temperature	T _j	+25 to +110°C

DC CHARACTERISTICS

Recommended operating conditions apply unless otherwise specified.

PARAMETER	CONDITIONS	MIN	NOM	MAX	UNIT
VCC1 Supply Current	Read Mode (V _{cc} ±5%)	23	28	33	mA
		19	28	37	mA
	Write Mode (V _{cc} ±5%)	21	24	27	mA
		17	24	31	mA
<i>*Head Select Pins (HS0, HS1) Floating</i>	*Idle Mode (V _{cc} ±5%)	6	9	12	mA
		4	9	14	mA
VCC2 Supply Current	Read Mode (V _{cc} ±5%)	5	8	11	mA
		4	8	12	mA
	Write Mode (V _{cc} ±5%)	6	8 + I _w	10 + I _w	mA
		5	8 + I _w	11 + I _w	mA
	Idle Mode (V _{cc} ±5%)	0.1	0.2	0.4	mA
		0.1	0.2	0.5	mA
Power Dissipation	Read Mode (V _{cc} ±5%)		175	230	mW
				270	mW

SSI 32R2030A/2031A

5V, 2, 4-Channel Thin-Film

Read/Write Device

DC CHARACTERISTICS (Continued)

PARAMETER	CONDITIONS	MIN	NOM	MAX	UNIT	
Power Dissipation (Continued)	Write Mode	(V _{CC} ±5%)		150 + 4I _w	190 + 4I _w	mW
		(V _{CC} ±10%)			230 + 4.4I _w	mW
	Idle Mode	(V _{CC} ±5%)		50	65	mW
		(V _{CC} ±10%)			80	mW
VCC1 Fault Voltage	I _w < 0.2 mA	3.8	4.0	4.2	VDC	

DIGITAL INPUTS

Input Low voltage (VIL)				0.8	VDC
Input High Voltage (VIH)		2.0			VDC
Input Low Current	VIL = 0.8V	-0.4			mA
Input High Current	VIH = 2.0V			100	μA
WUS Output Low Voltage (VOL)	I _{ol} = 2 mA max			0.5	VDC

WRITE CHARACTERISTICS

Recommended operating conditions apply unless otherwise specified.

Write Current Constant "K"		0.96	0.99		
Write Current Voltage (VWC)		1.15	1.25	1.35	V
WCADJ Voltage SSI 32R2031A/2031AR	I _{wCADJ} = 0 to .5 mA	2.0	V _{CC} /2	3.0	VDC
I _{head} (Decrease)/I _{wCADJ} SSI 32R2031A/2031AR		26	29	32	mA/mA
I _{wCADJ} Range SSI 32R2031A/2031AR		0.0		0.5	mA
Differential Head Voltage Swing	I _h (p-p) • R _h not to exceed 3.4V (Head Swing Min)	3.4			V _{pp}
Unselected Head Current				0.02 I _w	mApk
Head Differential Load Capacitance				25	pF
Head Differential Load	SSI 32R2030A/32R2031A	4K			Ω
Resistance (R _d)	SSI 32R2030AR/32R2031AR	560	700	950	Ω
WDI Pulse Width (Ref: Figure 1)	V _{il} = 0.2V, V _{ih} = 2.4V	PWH	37		ns
		PWL	5		ns
	V _{il} = 0.2V, V _{ih} = V _{CC}	PWH	20		ns
		PWL	5		ns
Write Current Range (I _w)		10		35	mA

SSI 32R2030A/2031A

5V, 2, 4-Channel Thin-Film

Read/Write Device

READ CHARACTERISTICS

Recommended operating conditions apply unless otherwise specified. CL (RDX, RDY) < 20 pF, RL (RDX, RDY) = 1 kΩ.

PARAMETER	CONDITIONS	MIN	NOM	MAX	UNIT
Differential Voltage Gain	Vin = 1 mVpp @1 MHz	200	250	300	V/V
Voltage BW	-1dB	Zs < 5Ω, Vin = 1 mVpp	20	60	MHz
	-3dB		35	70	MHz
Input Noise Voltage	BW = 15 MHz, Lh = 0, Rh = 0		0.6	0.85	nV/√Hz
Differential Input Capacitance	Vin = 1 mVpp, f = 5 MHz		27	35	pF
Differential Input Resistance	Vin = 1 mVpp, f = 5 MHz SSI 32R2030A/2031A	835	2600		Ω
	SSI 32R2030AR/2031AR	360	550		Ω
Dynamic Range	AC input voltage where gain falls to 90% of its small signal gain value, f = 5 MHz	3	6		mVpp
Common Mode Rejection Ratio	Vin = 0 VDC + 100 mVpp @ 5 MHz	45	80		dB
Power Supply Rejection Ratio	100 mVpp @ 5 MHz on VCC	40	70		dB
Channel Separation	Unselected channels driven with Vin = 0 VDC + 100 mVpp	45			dB
Output Offset Voltage		-300		+300	mV
Single Ended Output Resistance	f = 5 MHz			40	Ω
Output Current	AC coupled load, RDX to RDY	1.4			mA
RDX, RDY Common Mode Output Voltage		2.0	VCC1/2	3.5	VDC

SSI 32R2030A/2031A

5V, 2, 4-Channel Thin-Film Read/Write Device

SWITCHING CHARACTERISTICS

Recommended operating conditions apply unless otherwise specified. $I_W = 20\text{ mA}$, $L_h = 1.0\ \mu\text{H}$, $R_h = 30\ \Omega$
 $f(\text{Data}) = 5\ \text{MHz}$.

PARAMETER	CONDITIONS	MIN	NOM	MAX	UNIT	
R/W	Read to Write		0.1	1.0	μs	
	Write to Read		0.5	1.0	μs	
$\overline{\text{CS}}$	Unselect to Select		0.4	1.0	μs	
	Select to Unselect		0.4	1.0	μs	
HS0,1 to any Head	To 90% of 100 mV 10 MHz Read signal envelope		0.2	1.0	μs	
WUS:	Safe to Unsafe (TD1)	Write mode, loss of WDI transitions. Defines maximum WDI period for WUS operation	0.6	2.0	3.6	μs
	Unsafe to Safe (TD2)	Fault cleared from first neg WDI transition		0.2	1.0	μs
Head Current:		$L_h = 0$, $R_h = 0$				
	WDI to $I_x - I_y$ (TD3)	from 50% points		20	32	ns
	Asymmetry	WDI has 1 ns rise/fall time			1.0	ns
	Rise/fall Time	10% to 90% points		6	12	ns

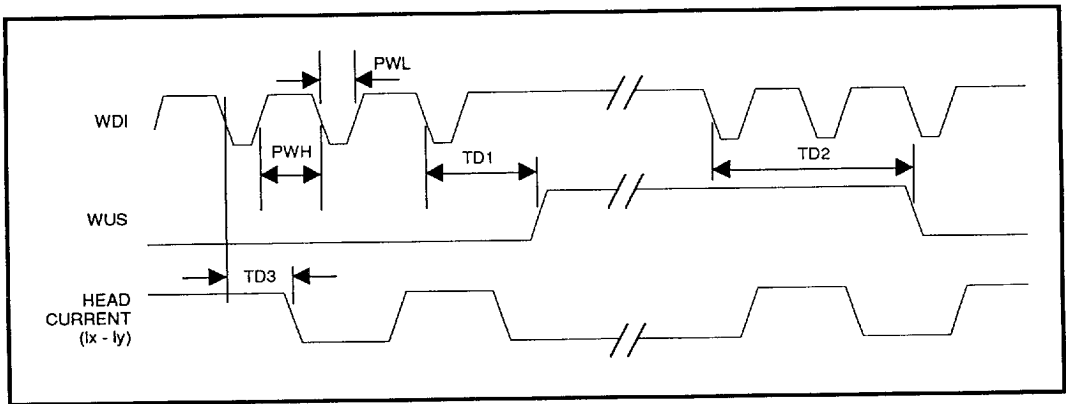


FIGURE 1: Write Mode Timing Diagram

SSI 32R2030A/2031A
5V, 2, 4-Channel Thin-Film
Read/Write Device

Worst Case Read Input Noise Voltage vs. Input Impedance for SSI 32R2030AR/2031AR

Case 1: IC Base sheet resistance = Maximum
Hence, IC bias Current = Minimum

	T _j = 25°C	T _j = 110°C	Units
Vn (Max)	.7	0.85	nV/√Hz
Rin (Min)	450	475	Ω
Cin (Max)	28	30	pF

Case 2: IC Base sheet resistance = Minimum
Hence, IC bias Current = Maximum

	T _j = 25°C	T _j = 110°C	Units
Vn (Max)	.58	.65	nV/√Hz
Rin (Min)	360	400	Ω
Cin (Max)	33	35	pF

Worst Case Read Input Noise Voltage vs. Input Impedance for SSI 32R2030A/2031A

Case 1: IC Base sheet resistance = Maximum
Hence, IC bias Current = Minimum

	T _j = 25°C	T _j = 110°C	Units
Vn (Max)	.7	0.85	nV/√Hz
Rin (Min)	1525	1895	Ω
Cin (Max)	28	30	pF

Case 2: IC Base sheet resistance = Minimum
Hence, IC bias Current = Maximum

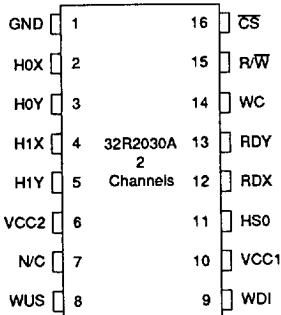
	T _j = 25°C	T _j = 110°C	Units
Vn (Max)	.58	.65	nV/√Hz
Rin (Min)	835	1100	Ω
Cin (Max)	33	35	pF

SSI 32R2030A/2031A

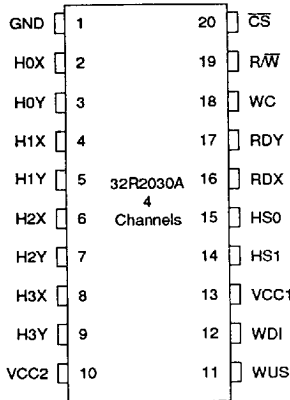
5V, 2, 4-Channel Thin-Film

Read/Write Device

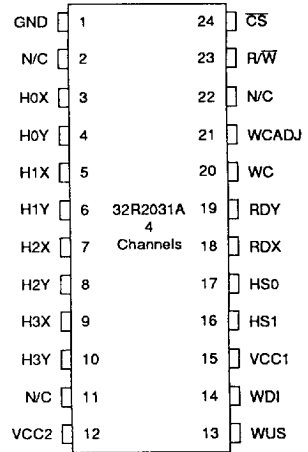
PACKAGE PIN DESIGNATIONS (Top View)



16-Pin SOL



20-Pin SOL, SOV



24-Pin SOL, SOV

THERMAL CHARACTERISTICS: θ_{ja}

16-Pin SOL	105°C/W
20-Pin SOL	95°C/W
20-Pin SOV	125°C/W
24-Pin SOL	80°C/W

ORDERING INFORMATION

PART DESCRIPTION	ORDER NUMBER	PACKAGE MARK
SSI 32R2030A		
16-Pin SOL	32R2030A-2CL	32R2030A-2CL
20-Pin SOL	32R2030A-4CL	32R2030A-4CL
20-Pin SOV	32R2030A-4CV	32R2030A-4CV
SSI 32R2031A		
24-Pin SOL	32R2031A-4CL	32R2031A-4CL
24-Pin SOV	32R2031A-4CV	32R2031A-4CV

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